

Customer No.: 31561  
Docket No.: 10894-US-PA  
Application No.: 10/604,796

**In the abstract of the disclosure**

Please amend the abstract of the disclosure as follows:

-- A microcontroller device for extending memory address space by inserting a waiting state and an operation method on the device. The device includes a CPU, a ROM, and a memory interface controller. In the device, the memory interface controller inserts a waiting state into the CPU when address of the information or the program to be fetched by the CPU is not located within a predetermined address range until the information or the program is completely fetched. --